Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	(("6567017") or ("6310856")).PN.	US-PGPUB; USPAT	OR	OFF	2008/01/23 14:42
L3	23612	select\$3 same (first bit line\$1 or first signal\$1 or first data pattern\$1 or first test pattern\$1 or first test vector\$1 or first vector\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/23 14:48
L4	24088	select\$3 same (second bit line\$1 or second signal\$1 or second data pattern\$1 or second test pattern\$1 or second test vector\$1 or second vector\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/23 14:48
L5	39	(transmit\$4 or communicat\$3 or send\$3) same (first selected signal\$1 or first selected data pattern or first selected vector or first selected test pattern)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/23 14:49
L7	2	(transmit\$4 or communicat\$3 or send\$3) and (first selected signal\$1 or first selected data pattern or first selected vector or first selected test pattern) and (selected bit line or selected path or selected transmission path)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/23 15:03
L8	31140	13 or 14	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/23 15:04
L9	19	18 and 15	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/23 15:04

	220	sianalina mattama	LIC DODUE	I ADI	ON	2009/01/22 15:04
L10	329	signaling pattern	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/23 15:04
L12	80136	memory controller	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/23 15:05
L13	1	l9 and l12	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/23 16:11
L14	36035	memory bus	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/23 16:11
L16	14632	write/read operation\$1 or read/write operation\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/23 16:12
L18	5098	(bus or path) near4 bit line\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON ·	2008/01/23 16:15
L19	47831	memory chip	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/23 16:15

L20	1221785	processor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/23 16:15
L21	23333	I19 and I20	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/23 16:15
L22	2042	l21 and (l14 or l18)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/23 16:15
L23	40	122 and 12	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/23 16:15
L24	22	123 and 14	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/23 16:30
L25	1	124 and 116	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/23 16:16
L26	1	I24 and I16	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/23 16:27

L28	38232	714/718 or 714/738 or 714/815 or 714/820 or 714/821 or 714/712 or 365/201 or 365/? or 711/100 or 711/? or 714/? or 375/?	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/23 16:33
L29		124 and 128	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/23 16:33